

**Amendment to the Specification**

At Page <sup>30</sup>~~10~~, the paragraph beginning at lines 32-33 is to be amended as follows:

Figure 6 is a plan view showing a relationship between a set of the tags shown in Figure

[[6a]] 5a and a field of view of a netpage sensing device in the form of a netpage pen;

Appln No. 10/815,647  
 Amdt. Dated October 6, 2006  
 Response to Office Action of September 21, 2006

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### Amendments to the Specification:

The paragraphs beginning at Page 1, line 7, through to Page 3 line <sup>9</sup>18, are to be amended as follows:

### CO-PENDING APPLICATIONS

Various methods, systems and apparatus relating to the present invention are disclosed in the following co-pending applications filed by the applicant or assignee of the present invention simultaneously with the present application:

<del>10/815,634,</del>	<del>10/815,632,</del>	<del>10/815,631,</del>
<del>10/815,648,</del>	<del>10/815,641,</del>	<del>10/815,645,</del>
<del>10/815,646,</del>	<del>10/815,617,</del>	<del>10/815,620,</del>
<del>10/815,615,</del>	<del>10/815,613,</del>	<del>10/815,633,</del>
<del>10/815,619,</del>	<del>10/815,616,</del>	<del>10/815,614,</del>
<del>10/815,631,</del>	<del>10/815,612,</del>	<del>10/815,630,</del>
<del>10/815,637,</del>	<del>10/815,638,</del>	<del>10/815,640,</del>
<del>10/815,642,</del>	<del>10/815,643,</del>	<del>10/815,644,</del>
<del>10/815,618,</del>	<del>10/815,639,</del>	<del>10/815,609,</del>
<del>10/815,627,</del>	<del>10/815,626,</del>	<del>10/815,610,</del>
<del>10/815,611,</del>	<del>10/815,623,</del>	<del>10/815,622,</del>
<del>10/815,620,</del>	<del>10/815,625,</del>	<del>10/815,624,</del>
<del>10/815,628,</del>	<del>10/815,636,</del>	<del>10/815,649,</del>
<del>10/815,635</del>		

<u>10/815621</u>	<u>10/815612</u>	<u>10/815630</u>	<u>10/815637</u>	<u>10/815638</u>	<u>10/815640</u>
<u>10/815642</u>	<u>7097094</u>	<u>10/815644</u>	<u>10/815618</u>	<u>10/815639</u>	<u>10/815635</u>
<u>10/815634</u>	<u>10/815632</u>	<u>10/815631</u>	<u>10/815648</u>	<u>10/815641</u>	<u>10/815645</u>
<u>10/815646</u>	<u>10/815617</u>	<u>10/815620</u>	<u>10/815615</u>	<u>10/815613</u>	<u>10/815633</u>
<u>10/815619</u>	<u>10/815616</u>	<u>10/815614</u>	<u>10/815636</u>	<u>10/815649</u>	<u>10/815609</u>
<u>10/815627</u>	<u>10/815626</u>	<u>10/815610</u>	<u>10/815611</u>	<u>7097106</u>	<u>7070110</u>
<u>10/815629</u>	<u>10/815625</u>	<u>10/815624</u>	<u>10/815628</u>		

The disclosures of these co-pending applications are incorporated herein by cross-reference.

**The Paragraph beginning at Page 45, lines 3-5, is to be amended as follows:**

Figure 54 shows the logical layout of another alternative hexagonal tag. This tag design is described in detail in the present applicants' co-pending US application USSN 10/786,631 /            entitled "Symmetric Tags" (~~docket number NPT037US~~).

*JA*  
*11/3/08*

**The Paragraph beginning at Page 104, lines 28-31, ~~through to Page 3, lines 1-3~~ is to be amended as follows:**

The imaging unit incorporates both the image sensor 2412 and the image processor 2410, which are usefully combined into a single compact chip as described in the co-pending US applications USSN 10/778,056 /            entitled "Image Sensor with Digital Frame Storestore", USSN 10/778,058 entitled "Image Sensor with Low-Pass Filter", USSN 10/778,060 entitled "Image Sensor with Range Expender", USSN 10/778,059 entitled "Pixel Sensor", USSN 10/778,063 entitled "Image Sensor for Timing Circuit", USSN 10/778,062 entitled "Image Processor with Low Power Mode", USSN 10/778,061 entitled "Image Processor", USSN 10/778,057 entitled "Synchronization Protocol" (~~docket no. NPS047 US~~ — NPS054), all filed 17 February 2004.